

FIG. 1
(PRIOR ART)

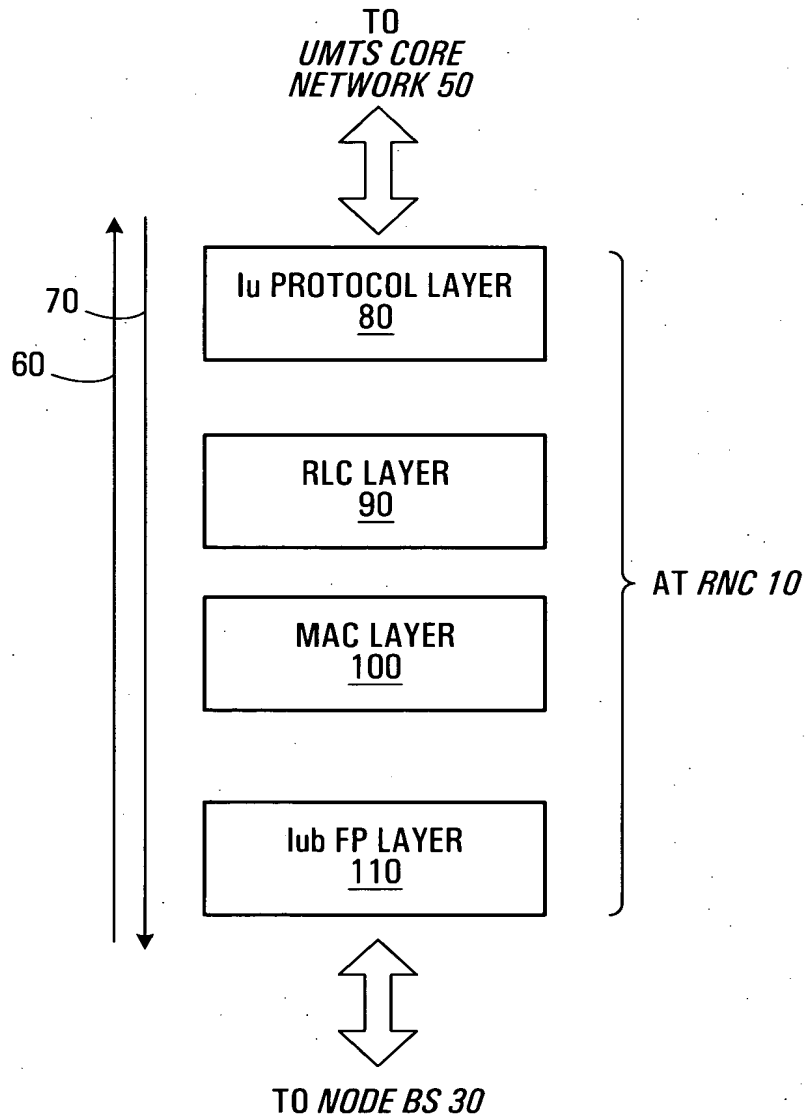
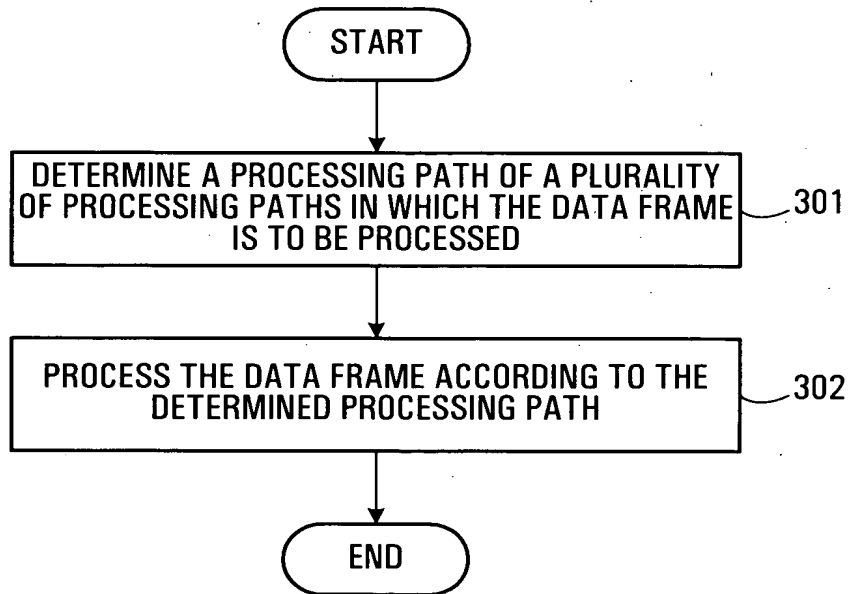


FIG. 2
(PRIOR ART)

**FIG. 3**

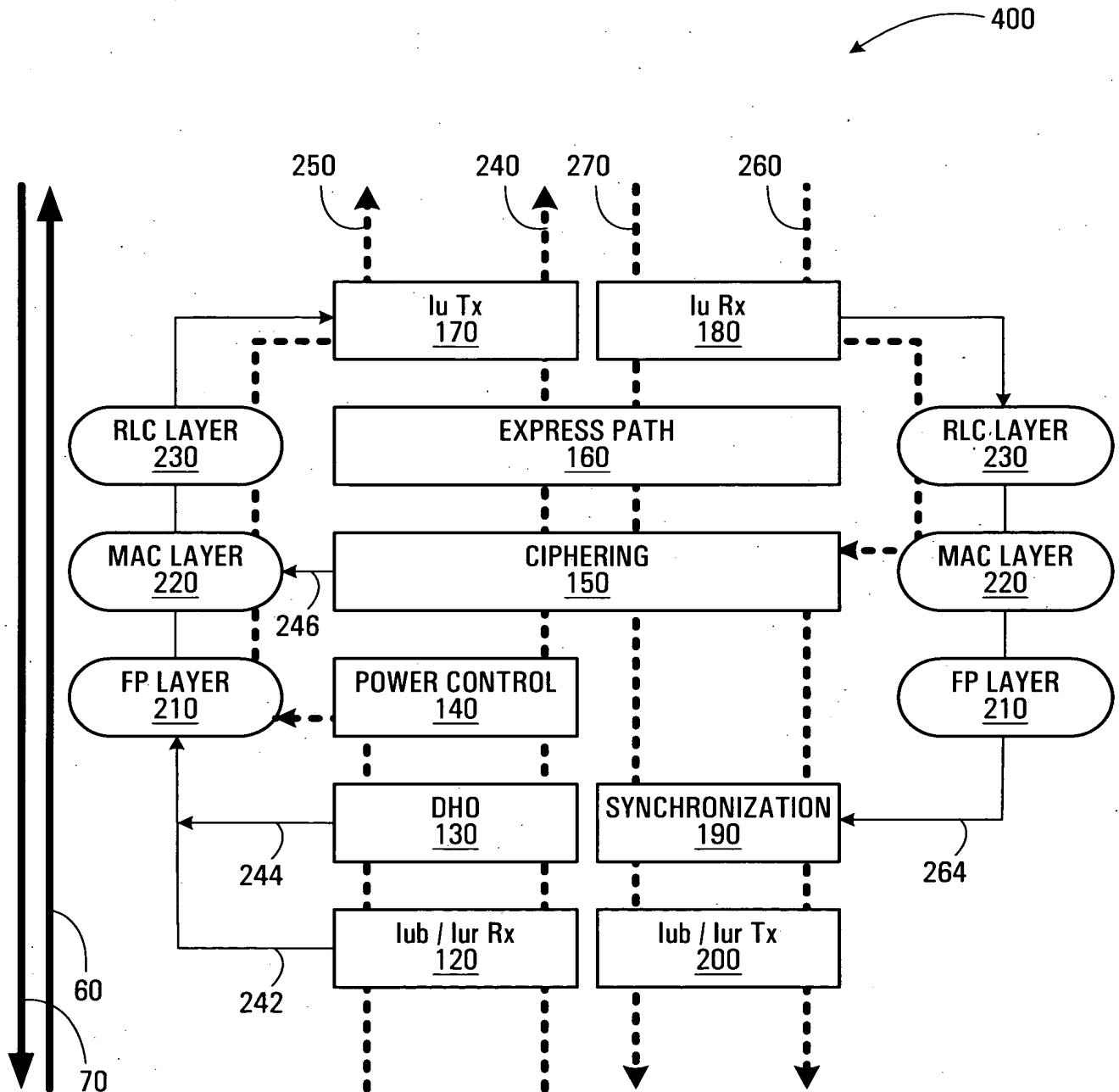
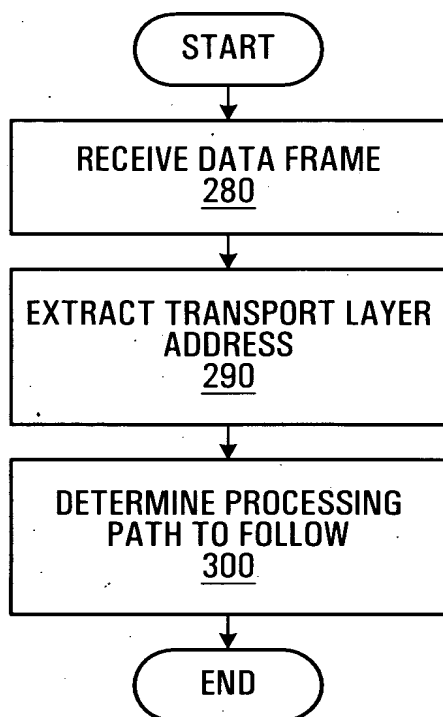
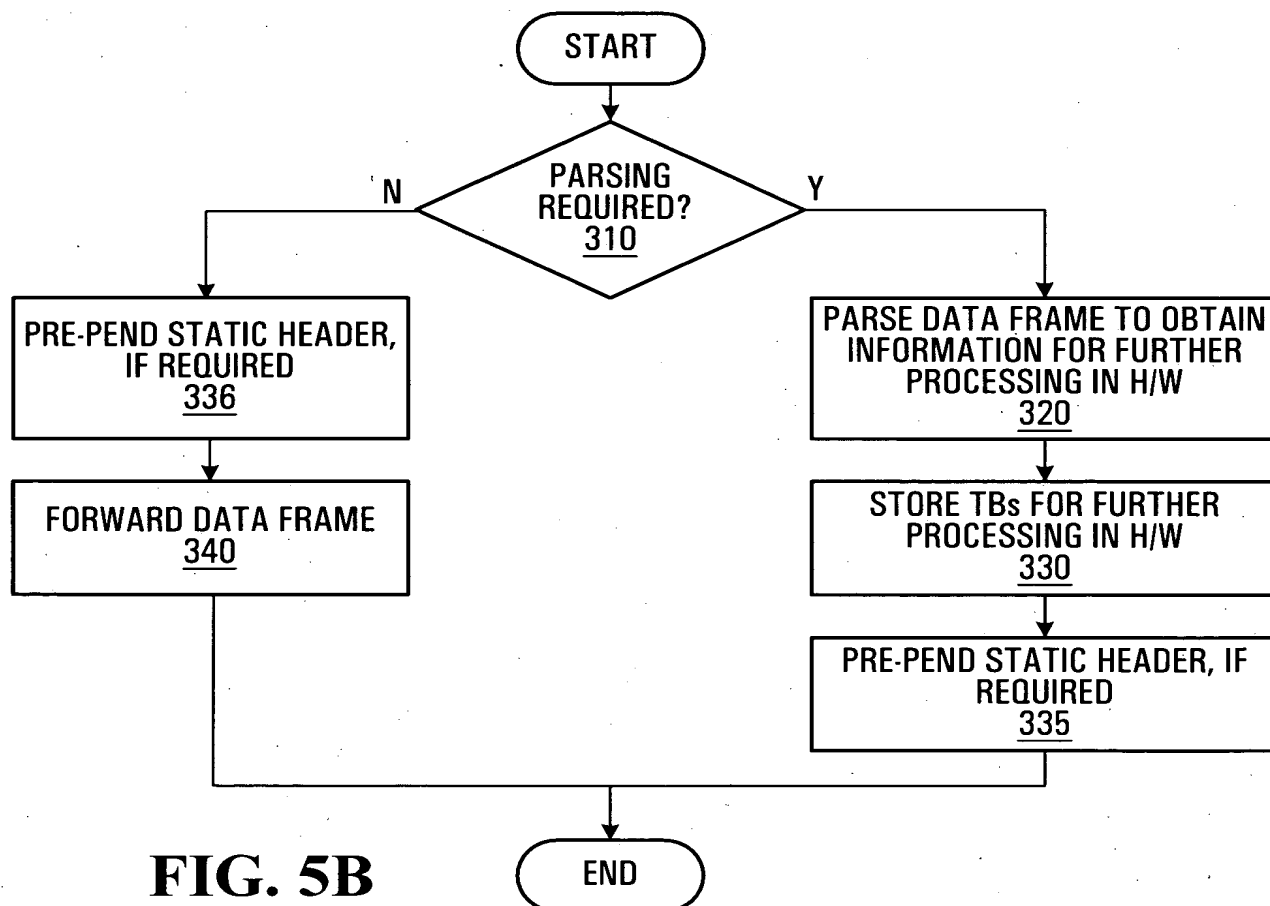


FIG. 4

**FIG. 5A****FIG. 5B**

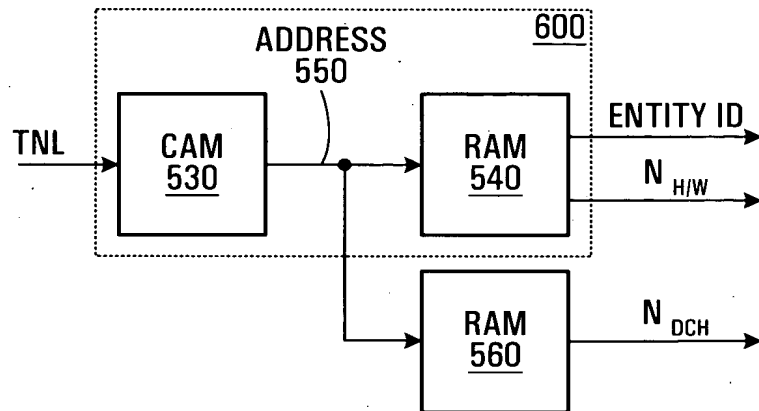


FIG. 6

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430

BITS								NUMBER OF OCTETS
7	6	5	4	3	2	1	0	
HEADER CRC <u>440</u>						FT <u>480</u>		1
CFN <u>450</u>								1
SPARE			TFI OF FIRST DCH <u>460</u>					0-n
...								
SPARE			TFI OF LAST DCH <u>460</u>					
FIRST TB OF FIRST DCH <u>470</u>								0-n
...								
FIRST TB OF FIRST DCH (CONT.) <u>470</u>					PADDING <u>570</u>			
...								0-n
LAST TB OF FIRST DCH <u>470</u>								0-n
...								
FIRST TB OF FIRST DCH (CONT.) <u>470</u>					PADDING <u>570</u>			
...								0-n
LAST TB OF FIRST DCH <u>470</u>								0-n
...								
FIRST TB OF LAST DCH (CONT.) <u>470</u>					PADDING <u>570</u>			
...								0-n
LAST TB OF LAST DCH <u>470</u>								0-n
...								
FIRST TB OF LAST DCH (CONT.) <u>470</u>					PADDING <u>570</u>			
QE <u>500</u>								1
								0-n
...								
					PADDING <u>570</u>			
SPARE EXTENSION <u>510</u>								0-2
PAYLOAD CHECKSUM <u>520</u>								2 (OPTIONAL)
PAYLOAD CHECKSUM (CONT.) <u>520</u>								

CRCI OF
FIRST TB OF
FIRST DCH
490

CRCI OF
LAST TB OF
FIRST DCH
490

FIG. 7
(PRIOR ART)

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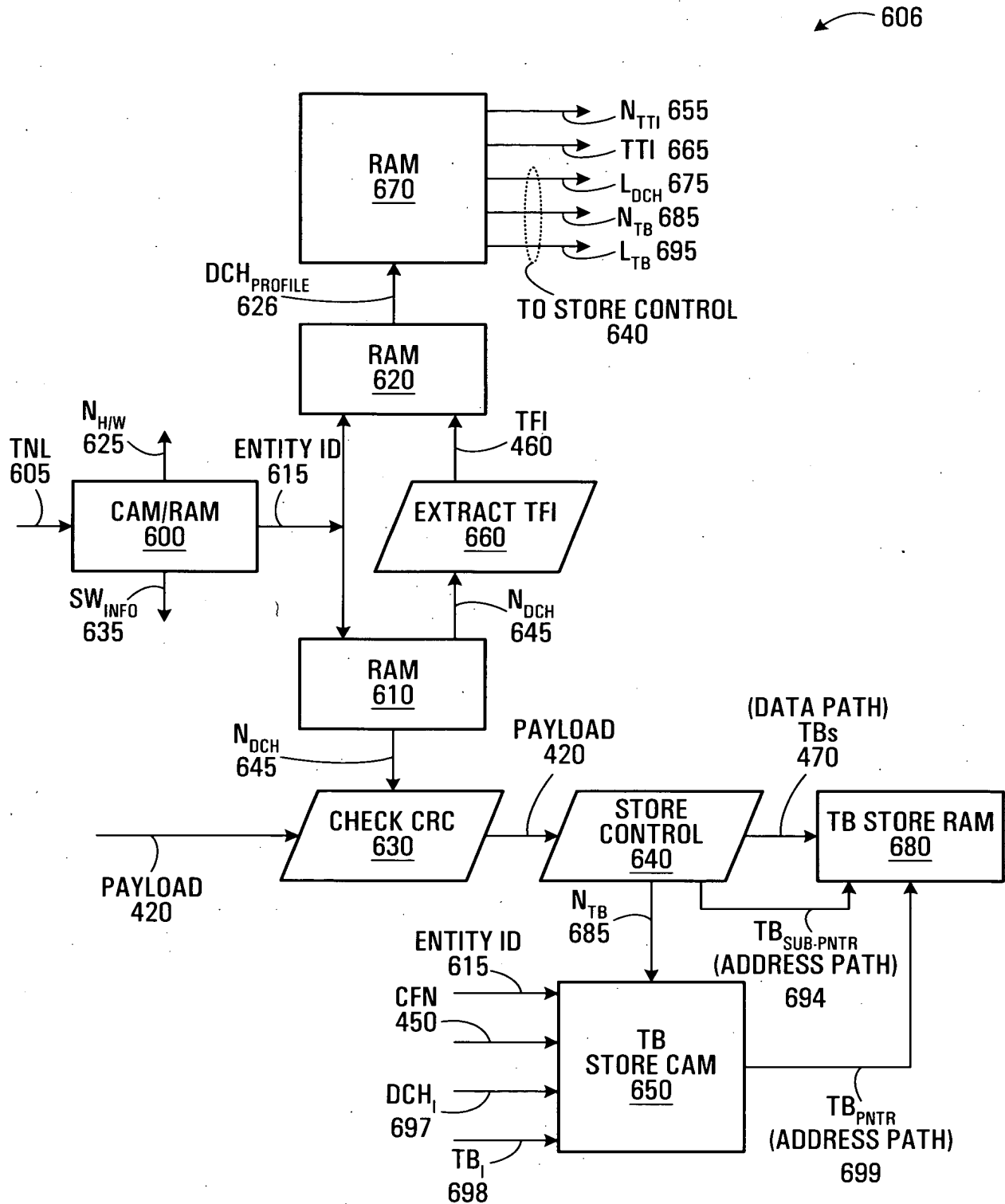


FIG. 8

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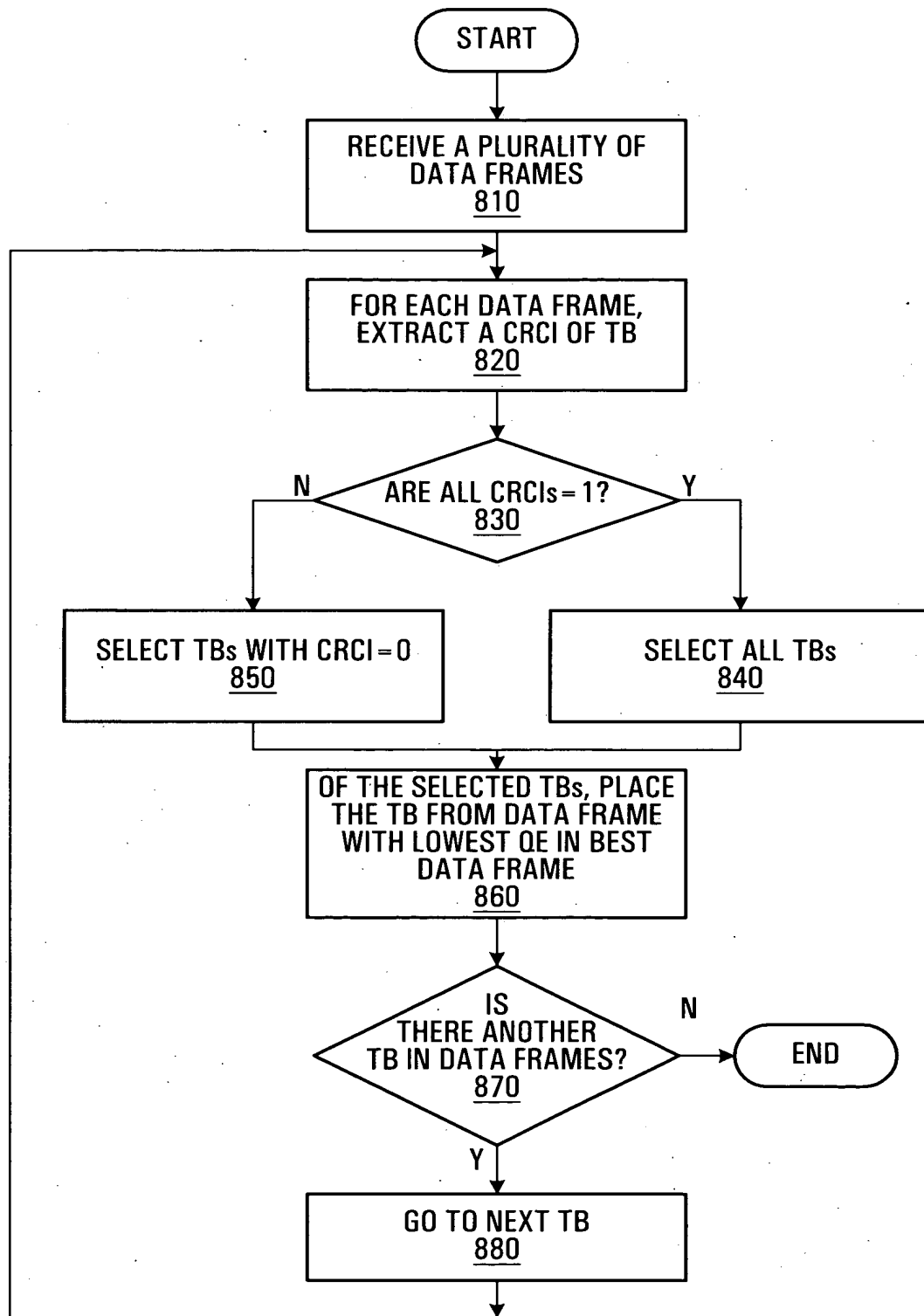


FIG. 9 (PRIOR ART)

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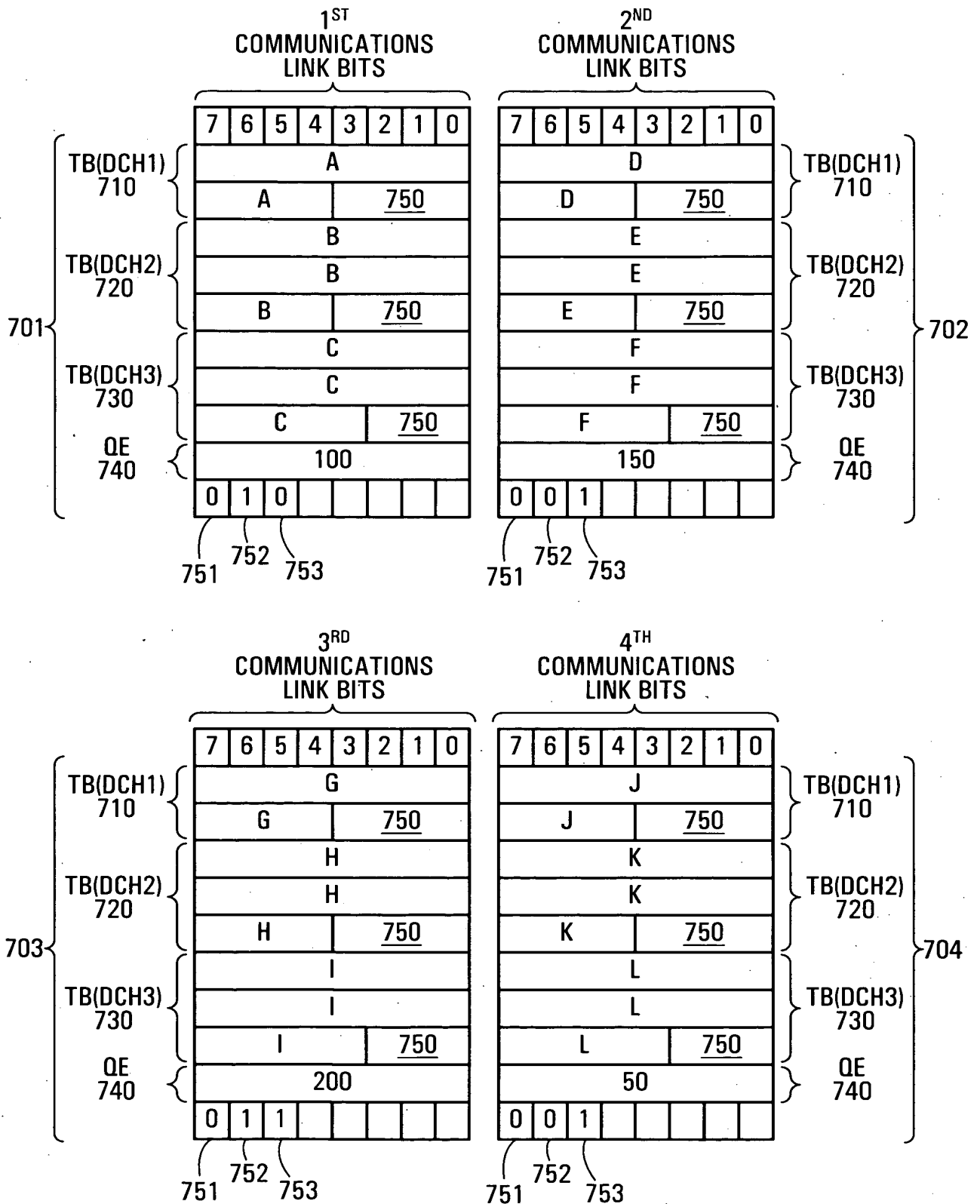


FIG. 10A (PRIOR ART)

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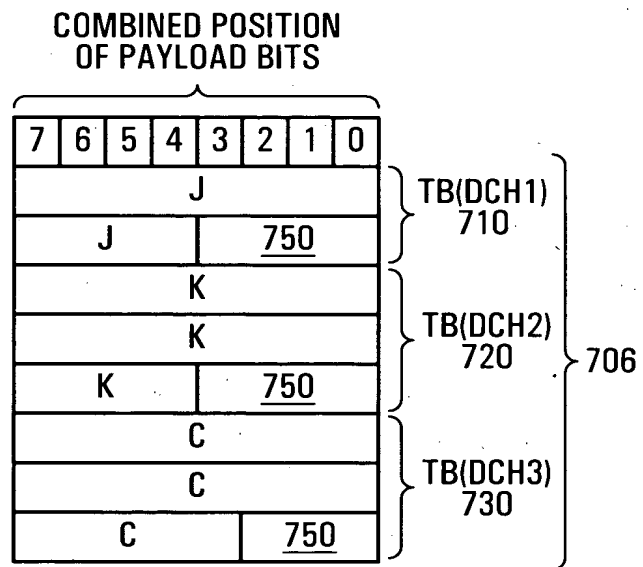


FIG. 10B
(PRIOR ART)

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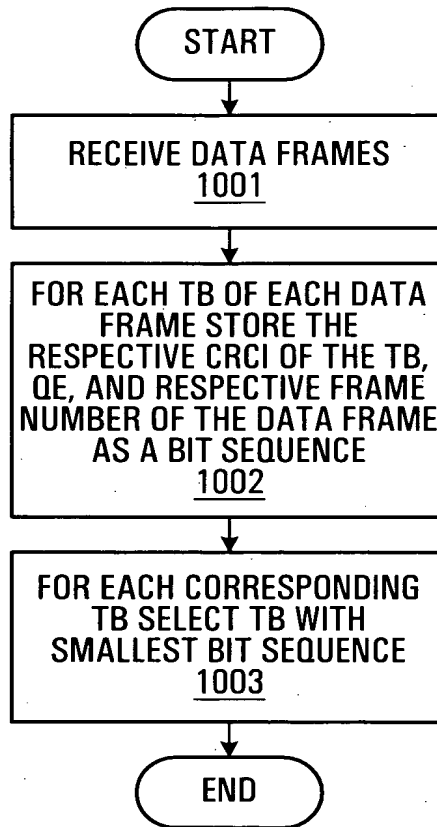


FIG. 11A

	1011	1031	1041
	C R C I	Q E	D A T A F R A M E N U M B E R
1021	1	100	1
1022	0	150	2
1023	1	200	3
1024	0	50	4
1050	15	14131211109876543210	
1061	1	0110010000000001	
1062	0	1001011000000010	
1063	1	1100010000000011	
1064	0	0001110100000100	
MINIMUM 1070	0	0001100100000100	
FILTER 1080			4

FIG. 11B

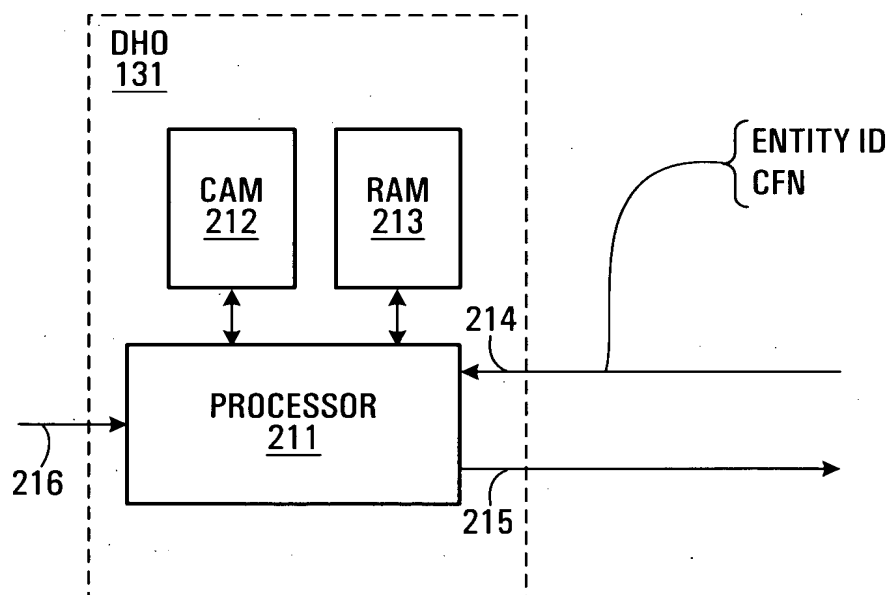


FIG. 11C

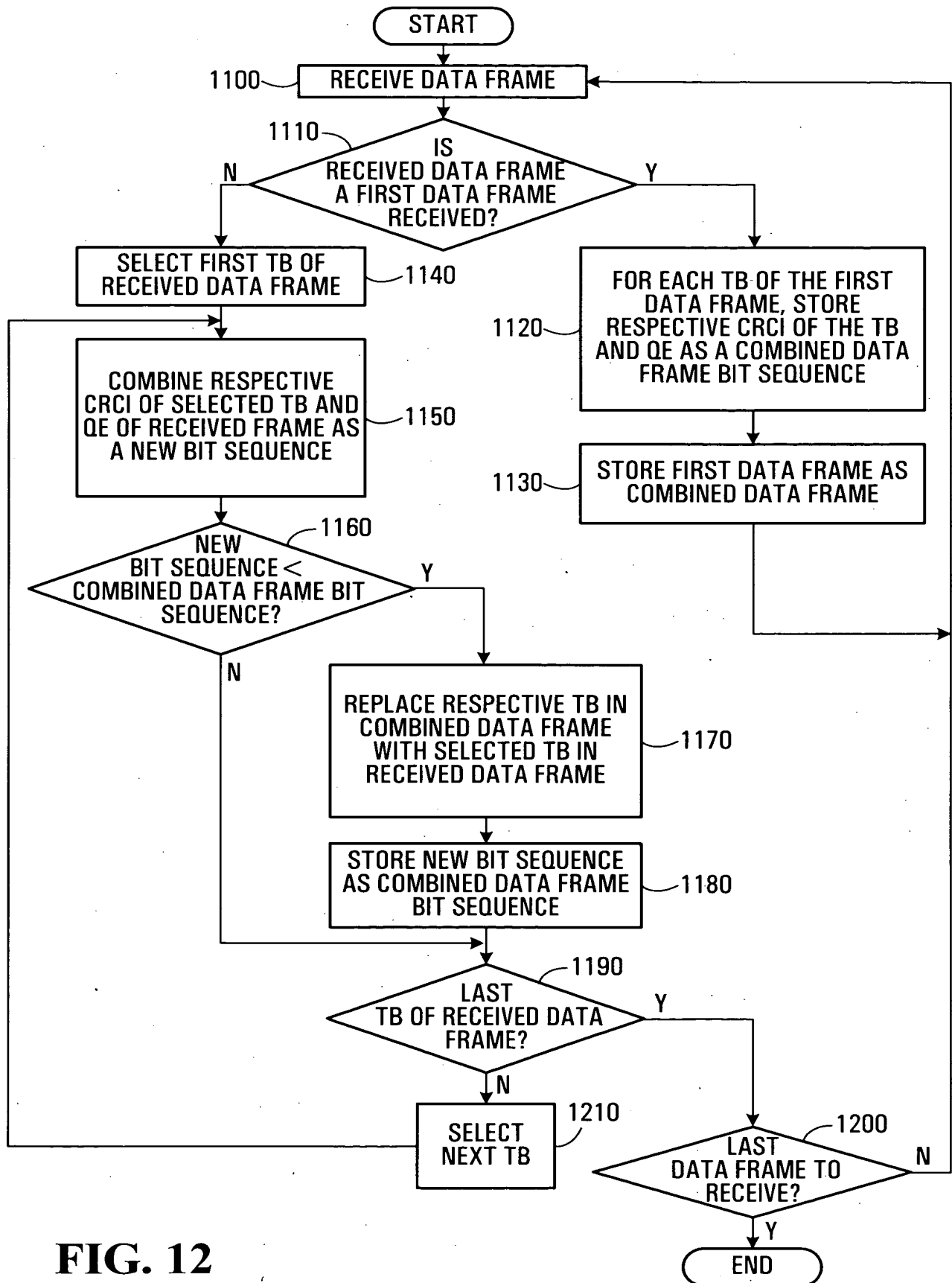


FIG. 12

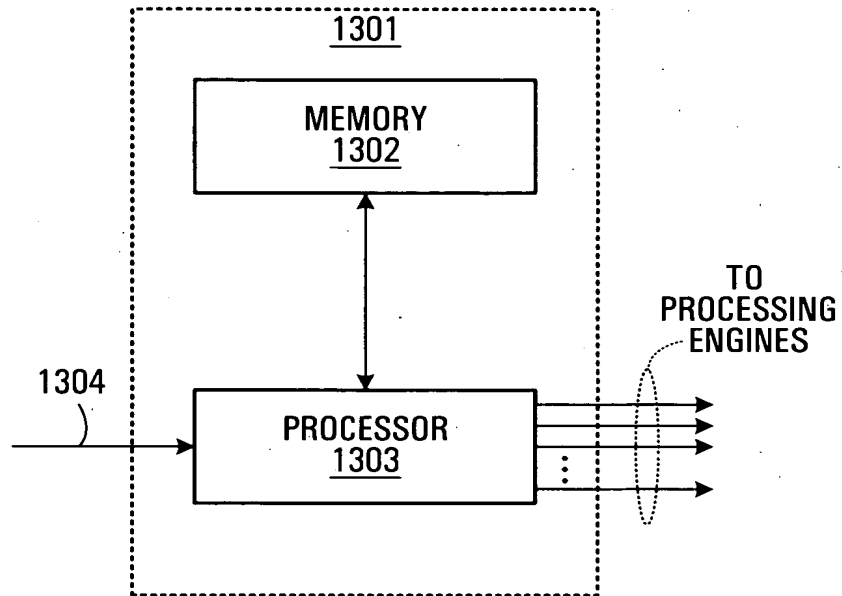


FIG. 13

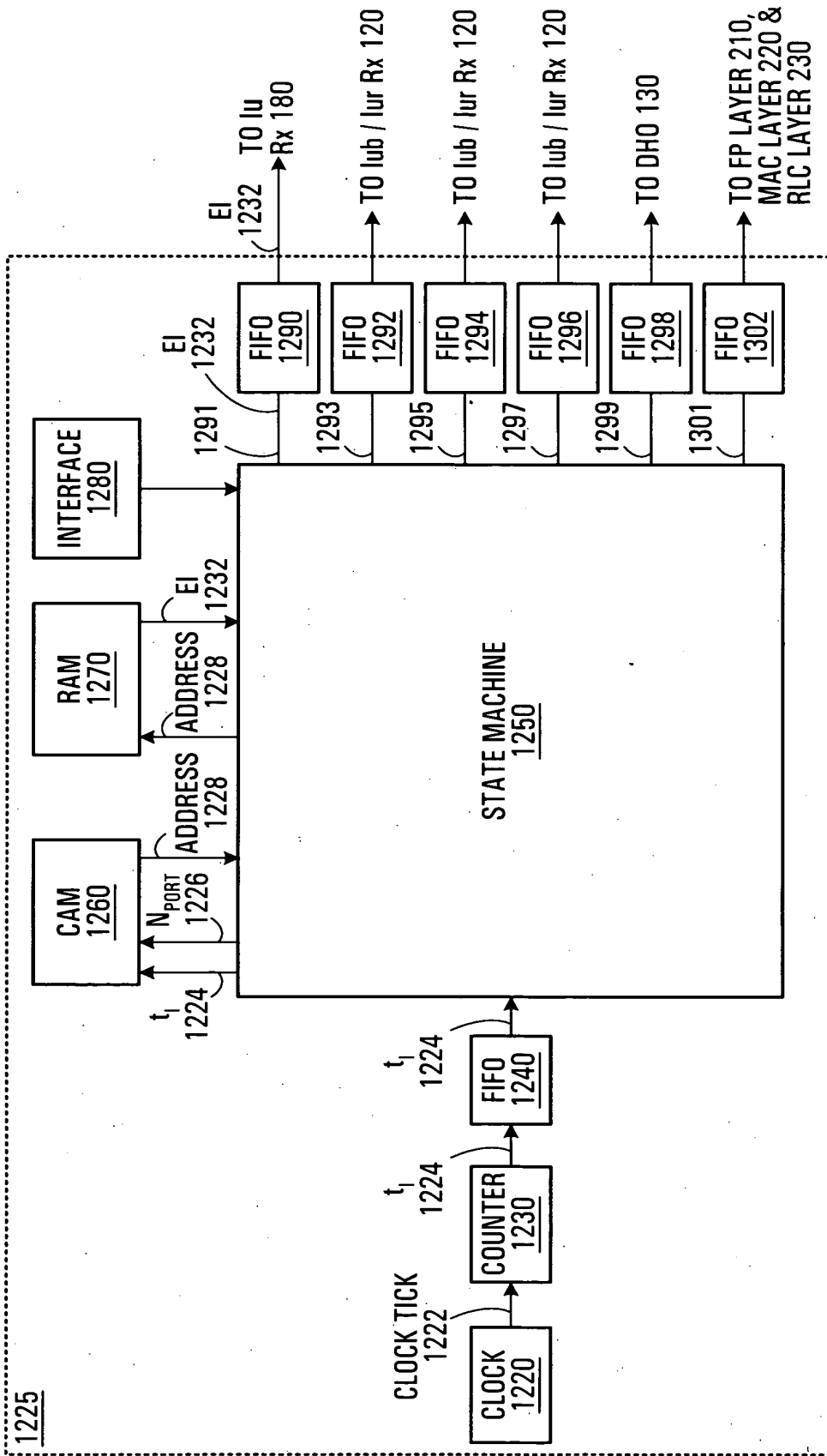


FIG. 14

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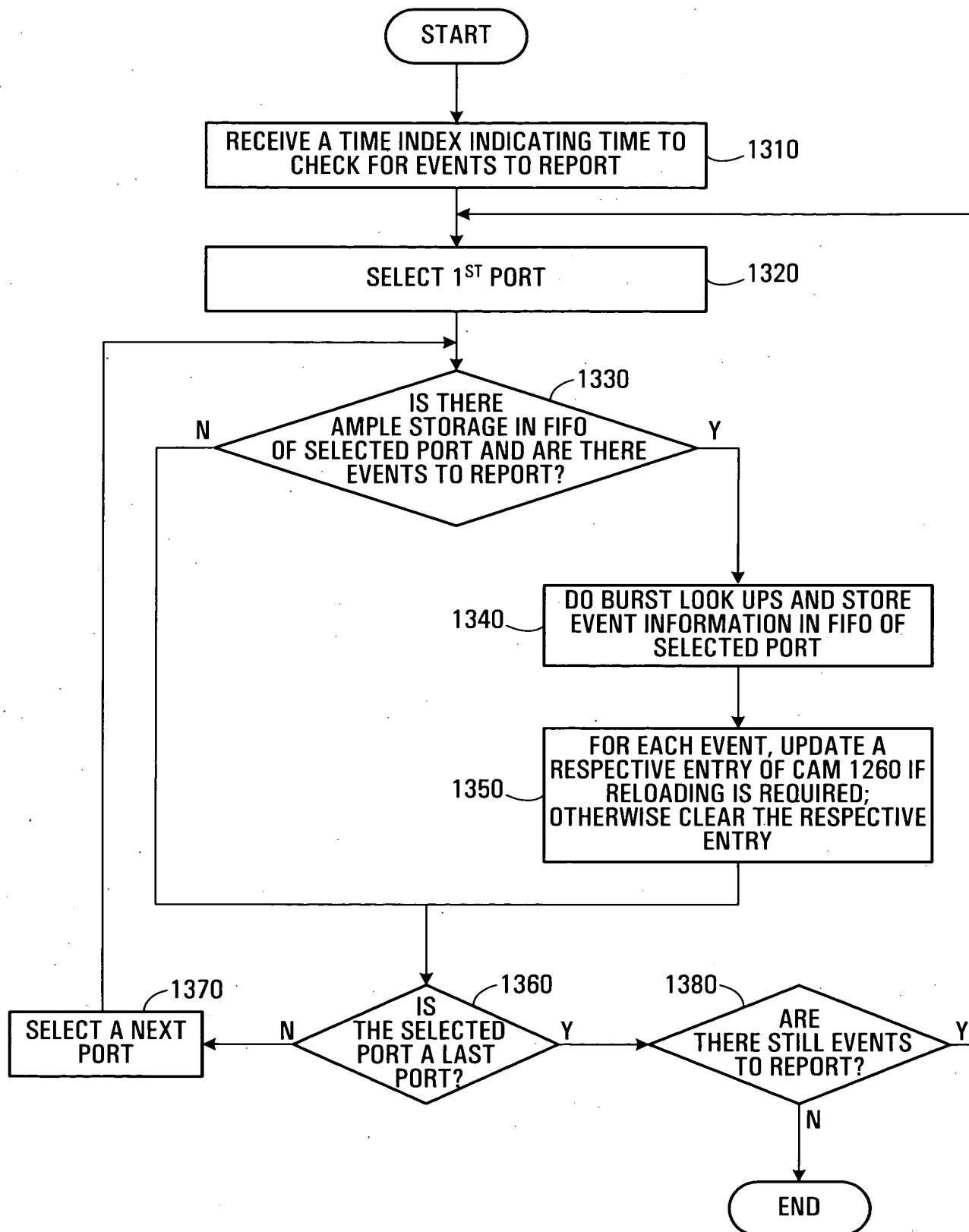


FIG. 15

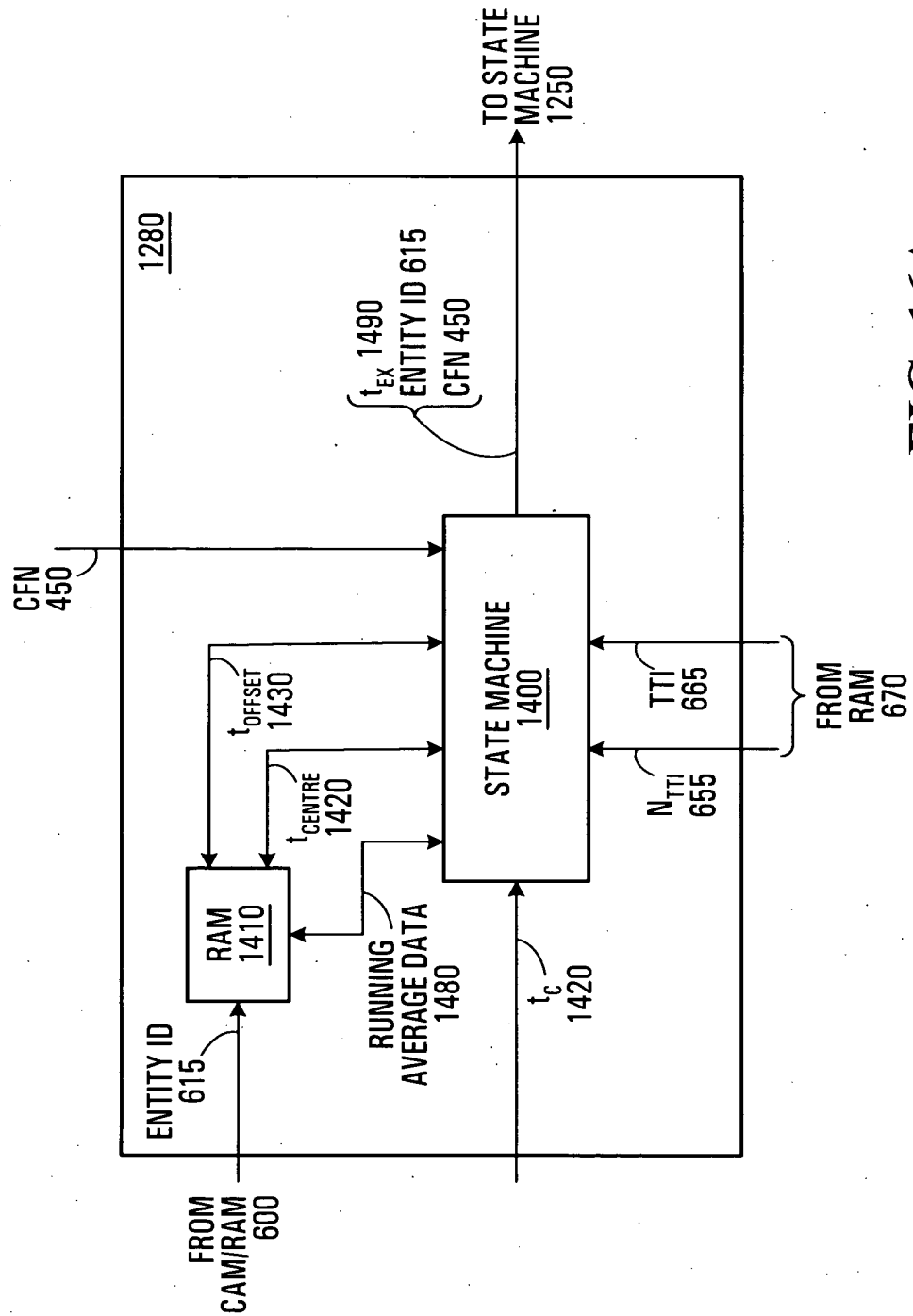


FIG. 16A

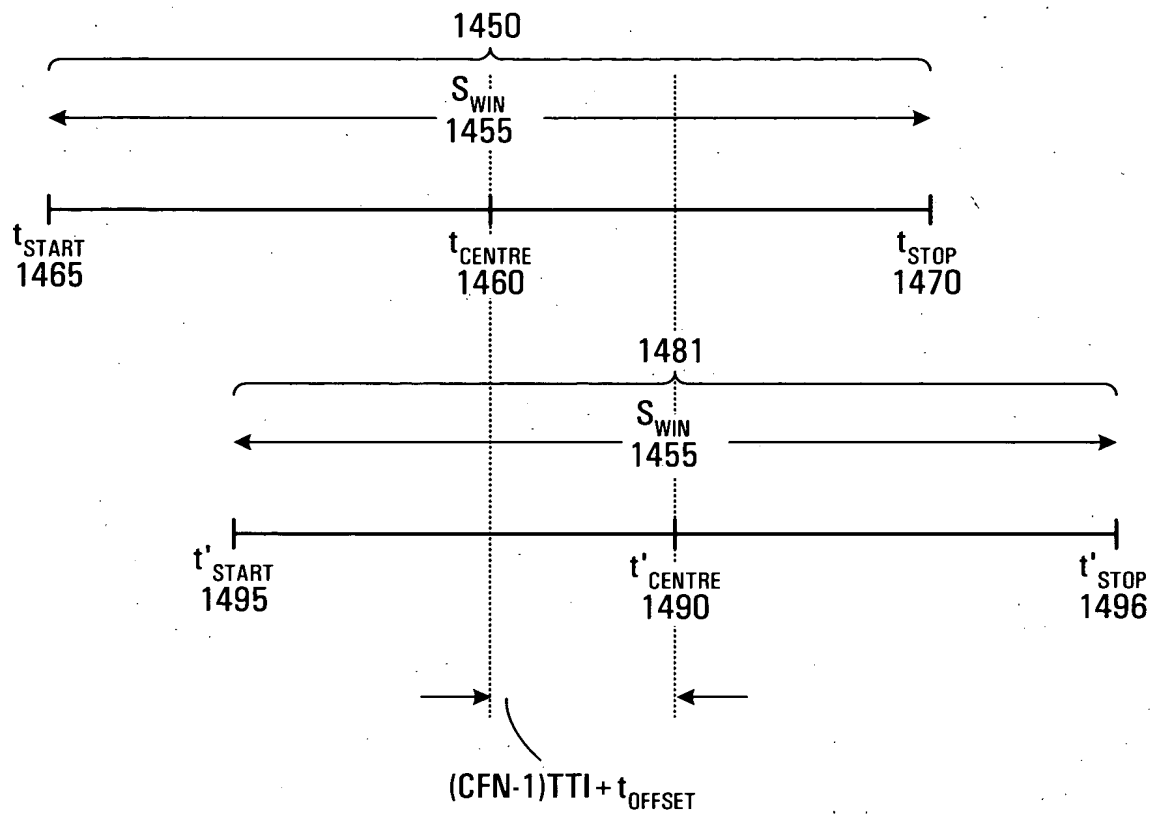


FIG. 16B